

ABSTRACT OF THE DISCLOSURE

In an offset control circuit, a voltage/current converting portion generates differential current (I^+ and I^-) that are proportional to a potential difference between differential input voltage signals (V_{IN}^+ and V_{IN}^-), and an offset adjusting current-generating portion generates offset adjusting currents (I_{ofs}^+ and I_{ofs}^-). In a current/voltage converting portion, a current (I_r) that is proportional to a potential difference between differential terminals flows through. Differential current output terminals, offset adjusting current-output terminals and the differential terminals are connected. The offset components contained in the differential input voltage signals (V_{IN}^+ and V_{IN}^-) are adjusted with the offset adjusting currents (I_{ofs}^+ and I_{ofs}^-), and differential output voltage signals (V_O^+ and V_O^-) in which the offset components are added to the differential input voltage signals (V_{IN}^+ and V_{IN}^-) are generated.